# **MA4X796** (MA796)

### Silicon epitaxial planar type

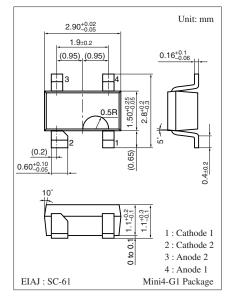
For super high speed switching For small current rectification

#### Features

- Two MA3X787 (MA787) is contained in one package (of a type in the same direction)
- Forward current (Average)  $I_{F(AV)} = 100 \text{ mA}$  rectification is possible
- Optimum for high frequency rectification because of its short reverse recovery time t<sub>rr</sub>
- Low forward voltage V<sub>F</sub> and good rectification efficiency
- Reverse voltage  $V_R = 50$  V is guaranteed

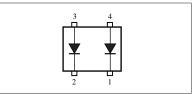
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Parameter		Symbol	Rating	Unit
Reverse voltage		V <sub>R</sub>	50	V
Repetitive peak reverse voltage		V <sub>RRM</sub>	50	V
Peak forward	Single	I <sub>FM</sub>	300	mA
current	Double *1		200	
Forward current	Single	I <sub>F(AV)</sub>	100	mA
(Average)	Double *1		70	
Non-repetitive peak forward surge current *2		I <sub>FSM</sub>	1	А
Junction temperature		Tj	125	°C
Storage temperature		T <sub>stg</sub>	-55 to +125	°C

#### Absolute Maximum Ratings $T_a = 25^{\circ}C$



#### Marking Symbol: M4B

#### Internal Connection



Note) \*1: Value of each diode in double diodes used.

\*2: The peak-to-peak value in one cycle of 50 Hz sine wave (non-repetitive)

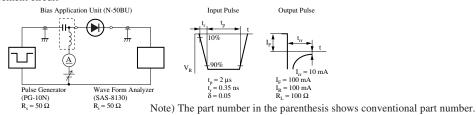
#### Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

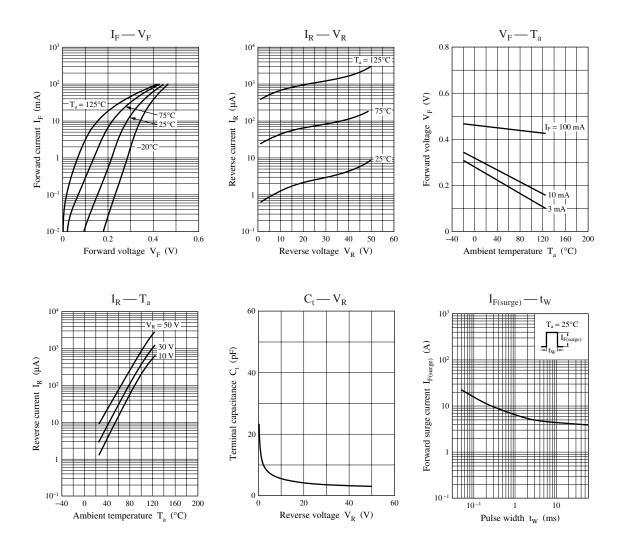
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	V <sub>F</sub>	$I_F = 100 \text{ mA}$			0.55	V
Reverse current	I <sub>R</sub>	$V_R = 50 V$			30	μΑ
Terminal capacitance	Ct	$V_R = 0 V, f = 1 MHz$		25		pF
Reverse recovery time *	t <sub>rr</sub>	$I_F = I_R = 100 \text{ mA}$		3.0		ns
		$I_{rr} = 10 \text{ mA}, R_L = 100 \Omega$				

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

2. This product is sensitive to electric shock (static electricity, etc.). Due attention must be paid on the charge of a human body and the leakage of current from the operating equipment.

- 3. Absolute frequency of input and output is 200 MHz.
- 4. \*: t<sub>rr</sub> measurement circuit





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